

TI-25320

Patent Amendment

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Park, et al.

Application No.: 09/173,129

Filed: 10/15/1998

Title: Selective Oxidation for Semiconductor  
Device Fabrication

Attorney Docket No.: TI-25320

Group Art Unit: 2814

Examiner: Peralta, G.

Tech Grp Fax: TC 2800 BF: 703-872-9318

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Date

Commissioner for Patents

Washington, D.C. 20231

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RESPONSE UNDER 37 CFR 1.111

TECHNOLOGY CENTER 2800

Dear Sir:

In response to the Office Action of November 5, 2002, please amend this  
application as follows:

In the claims:

Please substitute each of the following claims for the pending claim of the same number:

91 1 (Five Times Amended). A method of fabricating, in a semiconductor  
processing chamber, an electrical device formed in a semiconductor substrate, said  
method comprising:

forming an insulating layer over said semiconductor substrate;

To: Technology Center/Fax No.  
TC 2800 BF: 703-872-9318

Total Pages Sent: 15

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

Applicant: Park et al

Docket Number: TI-25320

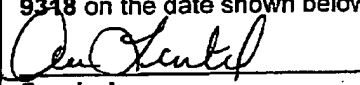
Serial No.: 09/173,129

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Filed: 10/15/1998

Examiner: Peralta, G.

For: Selective Oxidation for Semiconductor Device Fabrication

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**FACSIMILE COVER SHEET**

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<input type="checkbox"/> NEW APPLICATION	<input checked="" type="checkbox"/> EOT <u>(2 Pages (original + duplicate))</u>
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NAME OF INVENTOR(S):	
TITLE OF INVENTION:	
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RECEIPT DATE & SERIAL NO.: <b>Serial No.: 09/173,129</b> <b>Filing Date: 10/15/1998</b>	

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